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TITLE

U.S. UTILITY Patent Application

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PATENT DATE

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/677913		703	18	2123	BRODA

Gåry Ditlow Robert Proctor

Method of power distribution analysis for  ${\rm I/O}$  circuits in ASIC designs

PTO-2040 12/99

ISSUING CLASSIFICATION					
- ORIGIN	AL .		CROSS REFERENCE(S)		
CLASS	CLASS SUBCLASS CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
INTERNATIONA	L CLASSIFICATION				
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent				NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)			•		
The term of this patent shall not extend beyond the expiration date				·		
of U.S Patent. No.			ISSUE FEE			
<i>i</i>				Amount Due	Date Paid	
	(Primary Examiner) (Date)		(Date)		<u> </u>	
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this patent have been disclaimed.	(Legal instrum	ents Examiner)	(Date)			
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